

Travis Powers

Advanced Manufacturing Engineering Intern

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SUMMARY

Aspiring Chemical Engineering student committed to enhancing semiconductor processes through analytical problem-solving. Developed skills in standardizing procedures and improving equipment performance during academic projects. Hands-on experience collaborating with peers on innovative solutions that streamline operational reliability, alongside state-of-the-art methodologies for identifying production issues. Eager to apply knowledge in a full-time internship role at Tech Innovations Inc., contributing positively to the engineering team while pursuing continuous learning and professional growth. Future-driven individual excited about shaping advancements in the semiconductor industry.

EDUCATION

Bachelor of Science in Chemical Engineering

Syracuse University 🎓 GPA: 3.0 📅 2027 📍 Syracuse, NY

Coursework: Thermodynamics, Fluid Mechanics, Materials Science, Semiconductor Fabrication

TECHNICAL SKILLS

- **Software Tools:** MATLAB, AutoCAD, LabVIEW
- **Methodologies:** Lean Manufacturing, Six Sigma, Statistical Process Control
- **Laboratory Equipment:** Spectrometers, Microscopes, Oscilloscopes
- **Fabrication Techniques:** CVD, PVD, Etching
- **Younger Design Tools:** SolidWorks, ANSYS, COMSOL Multiphysics
- **Collaboration Platforms:** Trello, Asana, Microsoft Teams
- **Presentation Tools:** PowerPoint, Google Slides, Prezi
- **Data Visualization Software:** Tableau, Excel, Python Libraries
- **Quality Management Systems:** ISO 9001, TS 16949, AS9100
- **Health and Safety Standards:** OSHA Regulations, EPA Standards, NFPA Codes

SKILLS

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|----------------------------------|----------------------|---------------------------|---------------------------|
| • Process Improvement | • Project Management | • Problem Solving | • Effective Communication |
| • Equipment Performance Analysis | • Safety Compliance | • Research Methods | • Prototype Development |
| • Technical Documentation | • Data Analysis | • Lab Equipment Usage | • Performance Metrics |
| • Team Collaboration | • Innovative Design | • Streamlining Procedures | |

EXPERIENCE

Process Improvement Developer

University Project 📅 January 2026 - August 2026 📍 Syracuse, NY

Engaged in process improvement initiatives within semiconductor fabrication, focusing heavily on enhancing equipment performance and reliability. Collaborated effectively with a dynamic team, employing technical skills to standardize procedures and share research findings with faculty.

- Analyzed various semiconductor production processes, proposing innovative enhancement strategies for performance optimization.
- Implemented standardized operating procedures across different equipment types, resulting in seamless project transitions.
- Researched and reviewed variability-reducing recovery procedures, mitigating delays by improving response time during testing.
- Presented key project outcomes to academia, highlighting procedural improvements and strategic recommendations for ongoing enhancements.
- Ensured safety compliance throughout all project stages, fostering a secure and efficient research environment.
- Built effective communication channels within teams to facilitate collaboration and nurture an atmosphere of shared success.

Materials Science Research Assistant

University Research 📅 September 2025 - December 2025 📍 Syracuse, NY

Assisted researchers focused on new materials in semiconductor applications, contributing significantly to experimental design and data analysis efforts. Supported collaborative presentations, effectively refining organizational and teamwork skills.

- Regulated laboratory protocols ensuring accurate measurement and documentation of material properties under diverse conditions.
- Participated actively in developing presentation materials featuring experimental results for a high-profile university symposium.
- Maintained organized documentation of experiments, securing integrity which encouraged reproducibility of results for future studies.

- Facilitated weekly strategy sessions with team members for cohesive progress tracking and enhancement of research strategies.
- Diligently upheld all lab safety standards, promoting an environment centered around health and security during experiments.
- Leveraged prior knowledge of materials science to significantly support ongoing research endeavors.

Team Leader - Semiconductor Solutions

Hackathon Project 📅 March 2026 📍 Syracuse, NY

Led a motivated team through quick-paced challenges aimed at finding pioneering solutions for pressing semiconductor issues; leveraged innovative thinking and functionality-focused design to prototype game-changing systems.

- Guided team in rapid prototyping of devices for real-time performance metrics monitoring, emphasizing user-centric feedback loops.
- Coordinated group efforts efficiently under tight deadlines, implementing task prioritization to accelerate completion rates.
- Demonstrated leadership by outlining project objectives clearly and facilitating robust discussion around development iterations.
- Documented comprehensive reports detailing methodologies and distinctive findings central to addressing construction problems.
- Designed presentations that resonate well with diverse audiences, leading to recognition from judges for creativity in execution.
- Fostered an engaging team environment, maximizing group dynamics to boost collective problem-solving capabilities.

LEADERSHIP & AWARDS

- Dean’s List, Syracuse University - Fall 2025
- First Place, University Engineering Challenge - 2026

CERTIFICATIONS

- Certified in Process Safety Management (PSM) 📅 2026
- Lean Six Sigma Yellow Belt 📅 2026

PROFESSIONAL AFFILIATIONS

- Member, Chemical Engineering Society - 2025-Present
- Volunteer Tutor, Math and Science Tutoring Center - 2025-Present

LANGUAGES

- English (Native)

ADDITIONAL INFORMATION

Work Status : Authorized to work in United States. No sponsorship required.

REFERENCES

AVAILABLE ON REQUEST