

Zayd Kirk

Electrical Engineering Intern

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STRENGTHS

- 👥 **Collaborative Team Player**
Effective in bringing diverse minds together for problem-solving, recognized for leading discussion sessions that foster engagement.
- ⚡ **Strong Circuit Analysis Skills**
Adapted quickly to simulation software tools, gaining acknowledgment for accuracy in circuit investigations and reporting.
- ✔️ **Detail-Oriented Approach**
Practiced focus on fine details led to higher success in documentation quality, often praised by faculty mentors for thoroughness.
- 💬 **Engineering Communication Proficiency**
Skilled in preparing comprehensive reports, transforming complex findings into clear statements easily understood.
- 💡 **Resilient Problem Solver**
Exhibited ingenuity in troubleshooting obstacles faced during projects, helping steer teams toward fruitful resolutions.

SKILLS

Circuit Design MS Office Altium
OrCAD PSpice
Performance Testing
Engineering Documentation
Team Collaboration
Problem Solving Simulation Tools
Component Selection
Tolerance Studies

SUMMARY

Enthusiastic Electrical Engineering student with strong analytical skills and practical experience in electronic systems. Engaged in hands-on projects focusing on electronic device design, collaborating effectively with peers to achieve project goals. Demonstrated ability to conduct performance testing, prepare detailed engineering documents, and assess manufacturability of designs. Passionate about contributing innovative solutions to meet critical technology challenges. Committed to continuously expanding technical knowledge and expertise through academic research and university projects.

EDUCATION

Bachelor's Degree in Electrical Engineering

Cleveland State University 🎓 GPA: 3.8 📅 2026 📍 Cleveland, OH

Coursework: *Circuit Design, Signal Processing, Microprocessors, Digital Systems*

TECHNICAL SKILLS

- **Electrical Design Software:** Altium, OrCAD, PSpice
- **Performance Testing Tools:** LabVIEW, Multimeter, Oscilloscope
- **Documentation Tools:** MS Word, LaTeX, Google Docs
- **Components and Materials:** Resistors, Capacitors, Inductors
- **Simulation Environments:** MATLAB, LTspice, Simulink
- **Project Management Tools:** Trello, Asana, MS Project
- **Research Methodologies:** Experiment Design, Analytical Techniques, Test Validation
- **Collaboration Platforms:** Microsoft Teams, Zoom, Slack
- **Networking Knowledge:** RS-232, RS-485, USB
- **Safety Standards:** MIL-STD-882, IPC-A-610, ANSI/ESD S20.20

EXPERIENCE

Electrical Engineering Intern

University Project 📅 January 2026 - Present 📍 Cleveland, OH

Contributed to a team-oriented capstone project for designing an Electronic Safe and Arm Device (ESAD), targeting safety standards specific to missile applications. Employed simulations to validate circuit functionality while ensuring adherence to stringent industry requirements.

- Collaborated closely in designing and developing the ESAD, emphasizing reliability during system tests.
- Performed rigorous performance testing on circuits to confirm they met safety and operational criteria.
- Documented design processes through detailed engineering reports that facilitated knowledge sharing among peers.
- Analyzed impacts of electronic components under factors like shock, thermal stress, and vibration on devices.
- Actively participated in design reviews to explore enhancements for manufacturability of prototypes.
- Led efforts in coordinating lab trials, aiming to refine methodologies and improve results.

Research Assistant

Academic Research 📅 September 2025 - December 2025 📍 Cleveland, OH

Technical Proposals

Laboratory Protocols

Data Analysis Design Reviews

LANGUAGES

English Native

Spanish Proficient

MY CAREER



● Electrical Engineering Intern
at University Project (7 Months)

● Research Assistant at
Academic Research (3 Months)

Supported a faculty-backed initiative investigating Ignition Safety Devices (ISDs) within military contexts, zeroing in on the detailing of circuit design and necessary testing procedures.

- Assisted with various aspects of selecting components, concentrating on performance optimizations based on tolerance studies.
- Composite analysis of circuit performance was conducted through prior test result evaluations, helping adjust design parameters appropriately.
- Played an integral role in drafting proposals and presentations geared towards upcoming academic discussions.
- Collaborated with a multi-disciplinary team addressing both theoretical and practical challenges faced by high-reliability device integration.
- Contributed to protocol development targeted at validating performance under unpredictable conditions.
- Emphasized effective teamwork and communication as key drivers behind successful project milestones.

LEADERSHIP & AWARDS

- Dean's List recognition for spring 2025 semester, highlighting outstanding academic performance.
- Awarded first place in the Cleveland State University Engineering Design Competition for ESAD concept.

CERTIFICATIONS

- Certified LabVIEW Associate Developer 📅 2025
- Fundamentals of Engineering Exam Certificate 📅 2026

PROFESSIONAL AFFILIATIONS

- Member of the IEEE Student Chapter, regularly participating in workshops and networking events.
- Volunteer with local STEM outreach program promoting electronics education to high school students.

ADDITIONAL INFORMATION

Work Status : Authorized to work in United States. No sponsorship required.

REFERENCES

AVAILABLE ON REQUEST