

Adrian Bishop

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SUMMARY

Enthusiastic Electrical Engineering student currently engaged in hands-on hardware development and electrical testing. Experience spans designing, building, and rigorously evaluating electrical components through academic projects and internships. Utilizes essential electronics lab equipment effectively for testing while showcasing skills with PCB design software. Proven ability to collaborate across teams, contributing valuable insights in cross-discipline settings. Committed to making an impact within challenging tech environments that align with advancing space exploration technology.

EDUCATION

Bachelor's Degree in Electrical Engineering

2026

University of California, Los Angeles GPA: 3.85

Los Angeles, CA

Coursework: Electronics, Circuit Design, Signal Processing, Communication Systems

TECHNICAL SKILLS

- **Electronics Lab Tools:** Oscilloscopes, Multimeters, Power Supplies
- **PCB Design Software:** Altium Designer, Eagle, KiCAD
- **Simulation Software:** LTSpice, SPICE, PSpice
- **Prototyping Technologies:** Breadboarding, Soldering, Test Fixtures
- **Testing Methodologies:** Automated Testing, Manual Testing, Performance Evaluation
- **Documentation Standards:** Project Reports, Technical Manuals, Compliance Records
- **Collaboration Platforms:** Microsoft Teams, Slack, Zoom
- **Operating Systems:** Windows, Linux, macOS
- **Version Control:** Git, GitHub, Bitbucket
- **Quality Assurance:** ISO Standards, Lean Practices, Total Quality Management

SKILLS

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|---------------------------|----------------------|-----------------------------|----------------------------------|
| • PCB Design | • Team Collaboration | • Radiation Testing | • Test Planning |
| • Electrical Testing | • Problem Solving | • Lab Equipment Proficiency | • Automation Testing |
| • Circuit Simulation | • Soldering | • Data Analysis | • Cross-discipline Collaboration |
| • Technical Documentation | • Component Analysis | • Prototyping | |

EXPERIENCE

Electrical Engineering Intern

June 2025 - Present

University Innovation Lab

Los Angeles, CA

Contributes as an intern by executing hands-on testing of electrical circuits in a lab setting. Manipulates equipment including oscilloscopes and multimeters to diagnose issues and enhance component performance.

- Executed electrical test plans, ensuring meticulous documentation and presentation of results.
- Collaborated to create fault-tolerant circuit models, conducting simulations to determine behavior under multiple conditions.
- Engaged actively in breadboarding prototypes to apply theoretical principles practically.
- Participated in weekly reviews, assimilating team feedback to maximize project effectiveness.
- Produced detailed technical documents maintaining consistency throughout the developmental phases.
- Trained others on lab procedures, elevating operational standards and safety protocols.

Project Developer

September 2024 - May 2025

Capstone Project Group

Los Angeles, CA

Focused on the design and execution of a project analyzing radiation effects on semiconductor components. Enhanced research capabilities with performance simulations alongside practical hardware evaluations.

- Designed accurate models using LTSpice to forecast performance impacts in radiation environments.
- Built functional prototypes to critically assess electrical parameters and verify robustness post-exposure.
- Documented comprehensive findings effectively through peer-reviewed presentations.
- Leveraged Altium Designer for innovative PCB layouts adhering strictly to specifications.
- Collaborated seamlessly with peers, fostering problem-solving discussions that refined project outcomes.

- Critically reviewed technical documentation amplifying clarity and depth in reports.

Research Assistant

January 2024 - August 2024

Student Research Group

Los Angeles, CA

Supported research targeting how space radiation affects electronic components, working closely with faculty and research clusters. Engaged actively in hands-on experimentations and assembly of critical test setups.

- Assisted in constructing test fixtures central to assessing resilience against radiation exposure.
- Performed precise soldering and assembling of test harnesses to guarantee quality.
- Analyzed experimental data producing conclusions vital for ongoing studies and upcoming publications.
- Actively communicated goals within the team enhancing alignment and productivity.
- Participated energetically in workshops, accruing broader knowledge of advanced electronics.
- Contributed substantively to laboratory meetings, providing insights from recent data analyses.

LEADERSHIP & AWARDS

- Dean's List - University of California, Los Angeles, 2024
- First Place - University Hackathon, 2025

CERTIFICATIONS

- Certified in PCB Design Fundamentals 📅 2025
- Electronics Fundamentals 📅 2025

PROFESSIONAL AFFILIATIONS

- Member, IEEE Student Chapter, 2023 – Present
- Volunteer, STEM Outreach Program, 2023 – Present

LANGUAGES

- English (Native)
- Spanish (Intermediate)

ADDITIONAL INFORMATION

Work Status : Authorized to work in United States. No sponsorship required.

REFERENCES

AVAILABLE ON REQUEST