



Timothy York

Electrical Engineering Intern

📞 (512) 555-1234 ✉️ timothy.york@example.com
🌐 linkedin.com/in/timothy-york 📍 Austin, TX 78701

STRENGTHS

- 👥 Collaboration**
Frequently seek opportunities for collective input, enhancing project quality through teamwork.
- 📄 Technical Writing**
Excel in crafting clear and concise documentation, facilitating better understanding across diverse teams.
- 💡 Problem Solving**
Enjoy tackling engineering challenges creatively by breaking down problems for effective resolution.
- 🔗 Adaptability**
Adjust smoothly to evolving tasks and priorities, showcasing flexibility throughout various projects.
- 📖 Research Orientation**
Passionate about learning new methodologies, embracing advancements in technology during research projects.

SKILLS

- Embedded Systems
- Hardware Design
- Firmware Development
- Technical Documentation
- HWiL Testing
- Simulation Environments
- Team Collaboration
- Problem Solving Prototyping
- Avionics Document Management
- System Integration
- Project Specification

SUMMARY

Current Electrical Engineering student who thrives on collaborating with dynamic engineering teams. Have hands-on experience in hardware design, firmware development, and integration through academic projects. Proficient in creating technical documentation, running tests, and ensuring systems operate seamlessly together. Committed to learning and contributing effectively within local project teams and labs. Eager to deepen expertise in embedded systems and avionics while driving successful outcomes in real-world engineering tasks.

EDUCATION

Bachelor's Degree in Electrical Engineering
University of Texas at Austin 🎓 GPA: 3.8 📅 2026 📍 Austin, TX
Coursework: Circuits, Embedded Systems, Control Systems, Electromagnetics

TECHNICAL SKILLS

- Development Tools:** MATLAB, Simulink, LabVIEW, C++, Python, Arduino
- Testing Methodologies:** HWiL Testing, RTOS, Hardware Testing
- Documentation Tools:** Microsoft Word, LaTeX, JIRA
- Simulation Software:** Matlab Simulink, Multisim, NI Circuit Design
- Project Management Tools:** Trello, Asana, Microsoft Project
- Circuit Design:** Schematic Capture, PCB Layout, Spice Simulation
- Programming Languages:** C, C++, Python, MATLAB
- Embedded Systems:** ARM Cortex, Microcontrollers, FPGA Development
- Agile Methodology:** Scrum, Kanban, Agile Practices
- Version Control Systems:** Git, SVN

EXPERIENCE

Electrical Engineering Intern
University Project 📅 January 2026 - Present 📍 Austin, TX

- Role supports full-time engineers with robust involvement in the design and testing phases of electronic systems within an academic setting. Focuses on collaboration, implementation, and specification development in a student lab environment.
- Collaborated with engineers on designing and testing embedded systems that fulfilled requirements.
 - Assisted in software-hardware integration for multiple projects, ensuring all systems functioned optimally.
 - Conducted HWiL testing to validate performance within simulated design environments.
 - Developed clear technical documentation for project reports, enhancing communication of findings.
 - Designed test stands for avionics Circuit Card Assemblies, improving testing accuracy.
 - Engaged with team for aligning on task objectives, fostering excellent teamwork.

Research Assistant

- University Research Lab 📅 September 2025 - December 2025 📍 Austin, TX
- Provided crucial support for research initiatives focused on firmware functionality under senior engineers' guidance. Directly contributed to documenting, simulating designs, and peer reviewing to uphold engineering standards.
- Supported firmware development initiatives which notably influenced system enhancement.
 - Documented detailed research findings for seamless knowledge transfer among peers.
 - Conducted simulations to validate concepts as per theoretical design specifications.

Software Development

Testing Methodologies

LANGUAGES

English Native

Spanish Intermediate

MY CAREER



- Electrical Engineering Intern at University Project (7 Months)
- Research Assistant at University Research Lab (3 Months)

- Prepared presentations for faculty meetings, highlighting research outcome progress.
- Participated in peer reviews for quality assurance, ensuring compliance with engineering norms.
- Collaborated with multidisciplinary teams on addressing technical challenges efficiently.

LEADERSHIP & AWARDS

- Dean's List for Academic Excellence - Fall 2024
- Best Undergraduate Presentation Award - Spring 2025

CERTIFICATIONS

- Fundamentals of Engineering (FE) 📅 2025
- Certified LabVIEW Associate Developer 📅 2025

PROFESSIONAL AFFILIATIONS

- IEEE Student Member
- Engineering Student Council Member

ADDITIONAL INFORMATION

Work Status : Authorized to work in United States. No sponsorship required.

REFERENCES

AVAILABLE ON REQUEST